

PIN 1 -
PIN 2 -
PIN 3 -
PIN 4 -
PIN 5 -
PIN 6 -
PIN 7 -
PIN 8 -

600Ω/70.7V

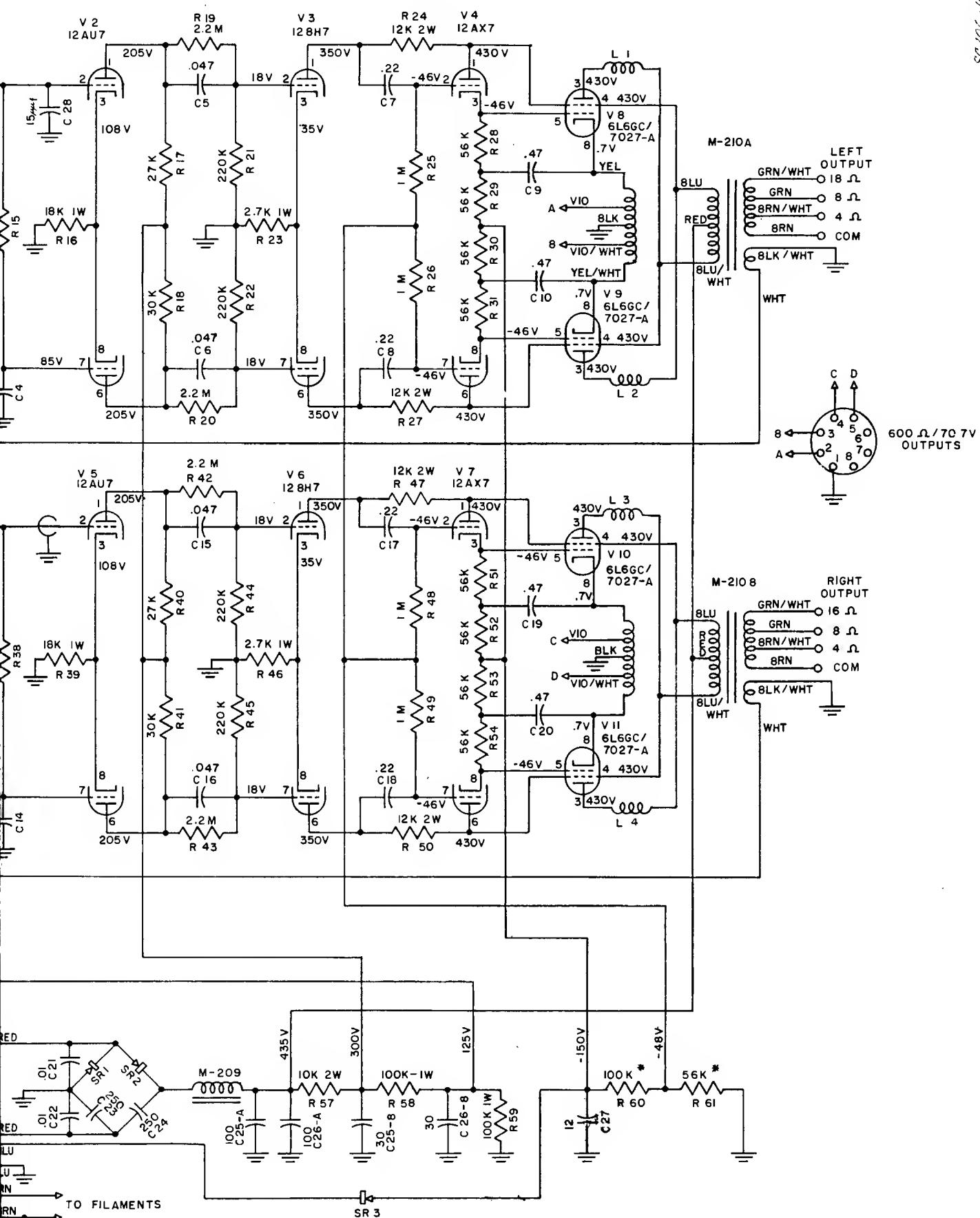
PIN 1 -
PINS 1 AND 2
PINS 3 AND 4
PINS 5 AND 6
PINS 7 AND 8

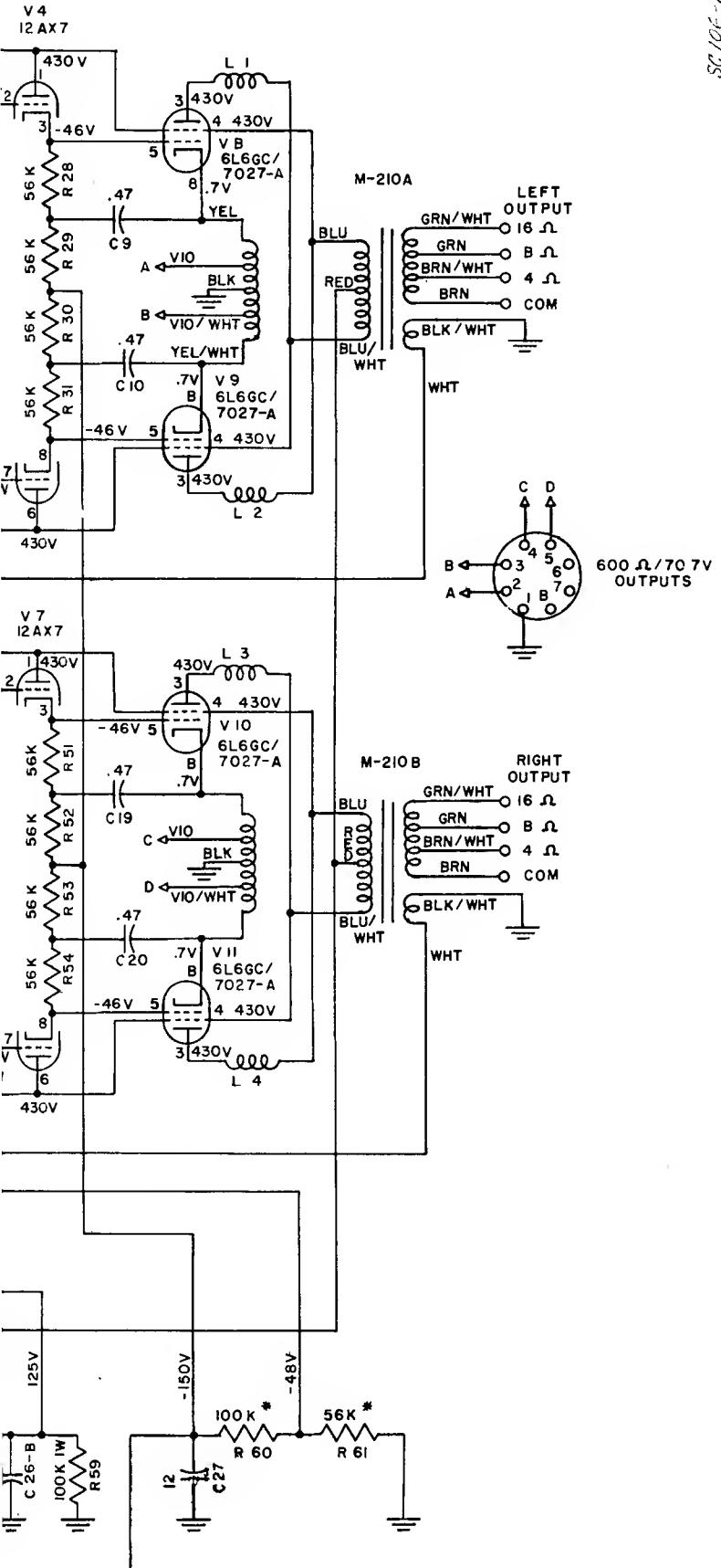
SINGLE CHANNEL

FOR SINGLE
PARALLEL
L AND R
DESIRED O

* 5% TOLERANCE

FOLLOWING
MATCHED
R47 AND





STEREO PREAMP POWER CONNECTIONS

PIN 1 - GROUND
PIN 2 - L INPUT
PIN 3 - R INPUT
PIN 4 - GROUND
PIN 5 - 375 V DC AT ISMA
PIN 6 - 25.2 V AT 1.2A
PIN 7 - 25.2 V CENTER TAP
PIN B - 25.2 V AT 1.2A

600Ω/70.7V OUTPUT CONNECTIONS

PIN 1 - GROUND, 70.7 V RETURN, AND 600Ω C.T.
PINS 1 AND 2 - LEFT 70.7 V OUTPUT
PINS 2 AND 3 - LEFT 600Ω OUTPUT
PINS 1 AND 4 - RIGHT 70.7 V OUTPUT
PINS 4 AND 5 - RIGHT 600Ω OUTPUT

SINGLE CHANNEL (MONO) OUTPUT NOTE:

FOR SINGLE CHANNEL OUTPUT
PARALLEL OR SERIES CONNECT THE
L AND R OUTPUTS TO OBTAIN
DESIRED OUTPUT IMPEDANCE.

* 5% TOLERANCE RESISTORS

FOLLOWING COMPONENTS ARE
MATCHED TO 1%: R24 AND R27,
R47 AND R50, R13 AND R36, R11 AND R34.

REVISIONS			MONITOR LABORATORY INC. 12 EAST 106 ST., BINGHAMTON, N.Y.		
NO.	DATE	BY	SCHEMATIC, MC-240		
1	9-26-60	TAK	DRAWN BY	VG	SCALE
2	12-20-60	TAK	CHKD BY	TAK	DATE
3	1-3-61	TAK	TRACED	7-13-60	MATERIAL
4	6-30-61	TAK	APP'D	SCIO	DRAWING NO.
5	1-24-61	TAK	APP'D	SAC	REV'D